



International Conference on
Computer Aided Design for Thin-Film Transistor Technologies
(CAD-TFT 2022)*

September 14-16, 2022 | Hybrid Meeting
Qingdao, China

**colocated with IFETC 2022, September 10-14, Qingdao, China*

Conference Objectives

The ever-increasing applications of large area electronics in commercial electronic products has drawn significant attention to thin-film transistors (TFT) and related technologies. TFTs on silicon, oxide and organic thin film technologies are becoming increasingly common. Besides applications in flat panel displays and imagers, new application areas are emerging, in which TFTs are used as analog and digital circuit elements for signal processing. This calls for reliable physically-based compact models of TFTs for expedient circuit simulation that accurately and reliably predicts system behavior. Concentrated R&D efforts are critical in order to develop TFT models for these thin-film technologies, and significant R&D efforts along these lines are underway worldwide.

The conference aims to:

- Promote research on CAD techniques at all levels for TFT technologies -- TCAD including materials and fabrication processes, compact modelling, circuit & system design).
- Train students, young researchers and engineers (knowledge and dissemination).
- Build international, academic-industry, and cross-level collaborations.
- Promote new applications of TFT technologies.

Conference Scope

This conference will provide a forum for discussions and current practices on compact modeling and to promote research on CAD techniques at all levels (TCAD including materials and processing, compact modelling, circuit & system design) of TFTs and related device and manufacturing technologies. It is also intended to promote the training of students, young researchers and engineers and to build international, academic-industry, and cross-level collaborations to evoke new applications of TFT technologies, including flexible electronics. The workshop is technically co-sponsored by the IEEE EDS Compact Modeling Technical Committee and Organic Electronics Technical Committee. A partial list of the areas of interest includes, but not limited to:

- Device physics of TFTs and operating principles.
- Compact TFT device models for circuit simulation.
- Model implementation and circuit analysis techniques.
- Model parameter extraction techniques.
- TFT design for displays and emerging applications, including flexible electronics.
- TCAD, including materials and processing.

CAD-TFT Conference Series

2006	Started in Cambridge, UK as the International Workshop on Compact TFT Modeling for Circuit Simulation
2008	Cambridge UK
2009	London, UK
2010	Tarragona, Spain
2012	Cambridge UK
2013	Grenoble, France
2014	Nanjing, China -- branded as International Conference on CAD for TFT Technologies (CAD-TFT)
2016	Beijing, China
2017	Cambridge, UK
2018	Shenzhen, China
2019	Tarragona, Spain
2020	Tianjin, China
2021	Cambridge, UK
2022	Qingdao, China

Thank You
and
Welcome to Qingdao!

Any suggestions or enquires, please feel free to contact:

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